



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the U.S. Patent and Trademark Office, Washington, D.C., 20231, on the below date of deposit.

Date of Deposit:	01/29/03	Name of Person Making the Deposit:	KATHERINE RINALDI	Signature of the Person Making the Deposit:	<i>Katherine Rinaldi</i>
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In re Application of: Kenneth Y. Ogami and Marat Zhaksilikov

Serial No.: 09/ 989,761

Examiner: not yet assigned

Filed: 11/19/01

Art Unit: 2171

For: STORING OF GLOBAL PARAMETER DEFAULTS AND USING THEM OVER TWO OR MORE DESIGN PROJECTS

Assistant Commissioner for Patents
Washington, D.C. 20231

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AMENDMENT TRANSMITTAL

Technology Center 2100

1. Transmitted herewith is an amendment for this application

☒ Transmitted herewith is a response to an office action for the above identified patent application.
(3 sheets)

☒ Transmitted herewith are sheets of Red Line drawings.

2. Applicant is other than a small entity

Extension of Term

3. The proceedings herein are for a patent application and the provisions of 37 C.F.R. 1.136 apply.

(a) [] Applicant petitions for an extension of time under 37 C.F.R. 1.136
(fees: 37 C.F.R. 1.17(a)-(d) for the total number of months checked below:)

<u>Extension</u>	<u>Fee</u>
[] one month	\$110.00
[] two months	\$410.00
[] three months	\$930.00
[] four months	\$1,450.00

Fee \$

If an additional extension of time is required, please consider this a petition therefor.

(b) [X] Applicant believes that no extension of term is required. However, this conditional petition is being made to provide for the possibility that applicant has inadvertently overlooked the need for a petition for extension of time.

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**Fee Calculation**

4. The fee for claims (37 C.F.R. 1.16(b)-(d)) has been calculated as shown below:

(for other than a small entity)					
Fee Items	Claims Remaining After Amendment	Highest Number of Claims Previously Paid For	Present Extra Claims	Fee Rate	Total
Total Claims	28	- 28 =	0	x \$18.00	\$0.00
Independent Claims	5	- 5 =	0	x \$84.00	\$0.00
Multiple Dependent Claim Fee (one or more, first added by this amendment)				\$260.00	\$0.00
Total Fees					\$0.00

PAYMENT OF FEES

5. The full fee due in connection with this communication is provided as follows:
- [x] The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085.
A duplicate copy of this authorization is enclosed.
- [] A check in the amount of \$
- [] Charge any fees required or credit any overpayments associated with this filing to Deposit Account No.: 23-0085.

Please direct all correspondence concerning the above-identified application to the following address:

WAGNER, MURABITO & HAO LLP
Two North Market Street, Third Floor
San Jose, California 95113
(408) 938-9060

Respectfully submitted,

Date: 1/29/2003

By: 

Anthony C. Murabito
Reg. No. 35,295

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PR-CD01179M

#4/PU-a
Q70ed
2/21/03

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF: :
Kenneth OGAMI et al. : GROUP ART UNIT:
SERIAL NO: 09/989,761 :
FILED: NOVEMBER 19, 2001 : EXAMINER:
FOR: STORING OF GLOBAL
PARAMETER DEFAULTS AND
USING THEM OVER TWO OR
MORE DESIGN PROJECTS

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PRELIMINARY AMENDMENT

COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

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SIR:

Prior to examination of the above-identified application, consideration of the following
Amendments and Remarks is respectfully requested.

IN THE SPECIFICATION

Please replace the paragraph beginning at page 2, line 5 with the following:

A1
US Patent Application Serial No. 10/033,027, entitled "PROGRAMMABLE
MICROCONTROLLER ARCHITECTURE," having docket number CYPR-CD00232, filed on
10/22/2001, and with inventor Warren Snyder is hereby incorporated by reference.

Please replace the paragraph beginning at page 11, line 14 with the following:

A2
In one embodiment, the system 100 is configured to support programmable microcontroller
design. In yet another embodiment, the system 100 supports general chip design.